

Electronic Acknowledgement Receipt

EFS ID:	2802464
Application Number:	10534962
International Application Number:	
Confirmation Number:	1696
Title of Invention:	Circuit board, multi-layer wiring boards, method of producing circuit boards and method of producing multilayer wiring boards
First Named Inventor/Applicant Name:	Masayoshi Kondo
Customer Number:	25461
Filer:	Robert G. Weilacher/Susan Revell
Filer Authorized By:	Robert G. Weilacher
Attorney Docket Number:	033036.088
Receipt Date:	01-FEB-2008
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Submitted with Payment	yes
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Payment was successfully received in RAM	\$460
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1		33036-088.PDF	543482 601266085e09e7217591c0c14d8b5d2726555344	yes	14
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Extension of Time		1	1	
	Amendment - After Non-Final Rejection		2	2	
	Amendment Copy Claims/Response to Suggested Claims		3	10	
	Applicant Arguments/Remarks Made in an Amendment		11	14	
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8210 0b057a0735a94od4545af1fda0af6bde37966bf9	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			551692		
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